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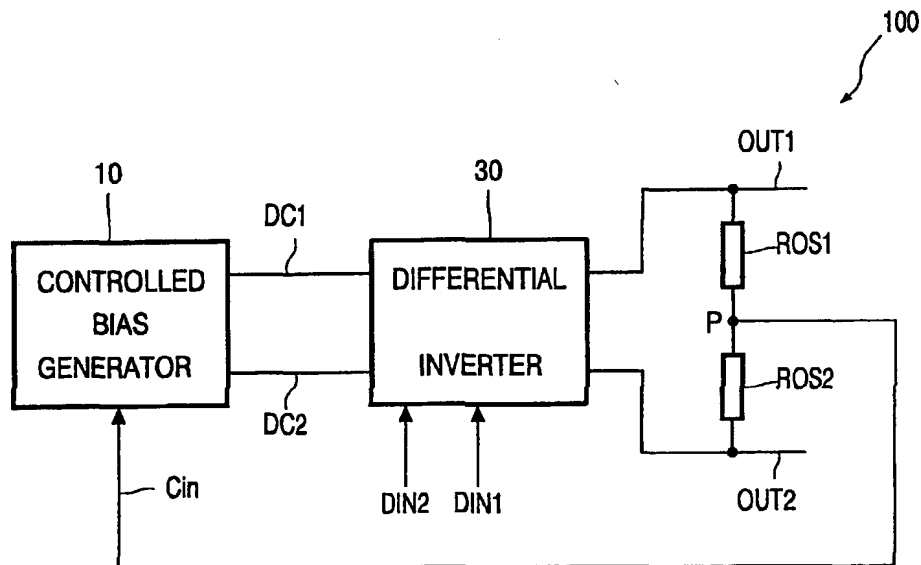
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[Continued on next page]

(54) Title: DIFFERENTIAL INVERTER CIRCUIT



(57) Abstract: A differential inverter/30) having a differential input for receiving a first input signal(DIN1) and a second input signal(DIN2), said inverter(30) further comprising a differential control input for receiving a first control signal (DC1 and a second control signal(DC2). The differential inverter further comprises a differential output for transmitting a first output signal (OUT1)and a second output signal(OUT2). The differential inverter further comprises a controlled bias generator(10)that generates the second vector of input signals in r esponse to a bias control signal(CIN). The control bias signal(CIN) is generated at an output of a voltage divider(ROS1,ROS2) coupled to the differential output of the differential inverter (30)said bias control signal(CIN) being indicative for a DC voltage of the of the differential output.



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INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER
 IPC 7 H03K3/012 H03F3/45 H03B5/12

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

| Category * | Citation of document, with indication, where appropriate, of the relevant passages | Relevant to claim No. |
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| X | US 5 936 466 A (ANDOH HAJIME ET AL) 10 August 1999 (1999-08-10) column 5, line 40 -column 7, line 44; figures 8,9 | 1-3 |
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